



Express Mail No. EV 346 793 432 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Eric NEYRET et al.

Confirmation No. 6832

Application No.: 10/671,813

Group Art Unit: 2814

Filing Date: September 25, 2003

Examiner: Dilinh P. Nguyen

For: METHOD FOR MINIMIZING SLIP LINE
FAULTS ON A SEMICONDUCTOR WAFER
SURFACE

Attorney Docket No.: 4717-7800

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450


Sir:

Pursuant to applicants' duty of disclosure under 37 C.F.R. 1.56, enclosed is a Form PTO-1449 listing one (1) reference for the Examiner's review and consideration. It is respectfully requested that this reference be made of record in this application by the Examiner's completion and return of the PTO Form 1449.

A fee of \$180 believed to be due for this submission since the attached reference is being submitted while a non-final Office Action is pending in the present application. Please charge such fee to **Winston & Strawn LLP** Deposit Account No. 50-1814.

Respectfully submitted,

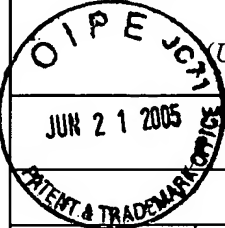
Date: June 21, 2005


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LIST OF REFERENCES CITED BY APPLICANT Form PTO-1449 <i>(Use several sheets if necessary)</i>				ATTY. DOCKET NO.: 4717-7800		APPLICATION NO.: 10/671,813	
				APPLICANT: Eric NEYRET et al.			
				FILING DATE: September 25, 2003		GROUP: 2814	
U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	CITE NO.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	2002/0009841	1/2002	Hayashi et al.	438	189	
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO
OTHER REFERENCES <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>							
EXAMINER				DATE CONSIDERED			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							